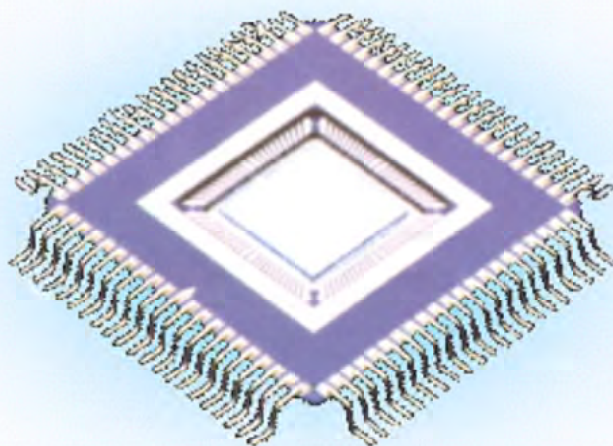




MYK-41

DES VLSI Microcircuit



Specifications

Data Rate

1 bps to 160 Mbps

Operating Voltage

4.5 to 5.5 V DC

Power Consumption

40 mW/MHz (nominal)

Data Interface

I/O: 32-bit parallel DTL
Clocks: CMOS

Format

NRZ-L

Testing

Functional and
Propagation Delays

Group E and DPA
also available

Temperature Range

-55 to +125° C

Technology

Sub-micron CMOS

Package

172-Pin Ceramic Flat Pack

Satellite Downlink Encryptor/Decryptor

Cryptographically compatible with DES FIPS Pub 46

Features

- Operates in Electronic Codebook (ECB) or Output Feedback (OFB) mode
- Off-the-shelf QML "Q" availability
- Maximum data rate of 160 Mbps with system clock at 40 MHz
- Subject to export controls
- Meets QML Class A requirements of MIL-PRF-38535
- DESC-qualified JAN CMOS gate array manufacturer
- Radiation hardened for space applications

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PRODUCT BRIEF